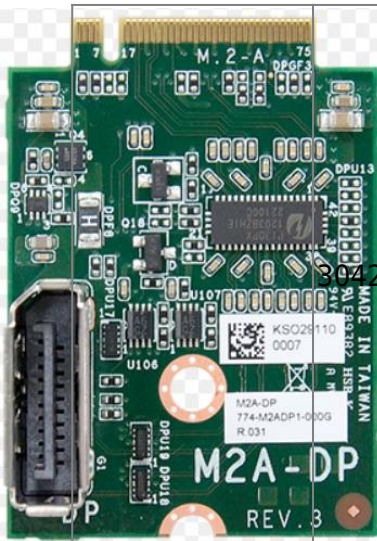


Содержание

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2242 Key A DP

Table 23. Display Port Based Module Solution Pinout (Module Key A)

74	1.5V	GN0	75
72	1.5V	REFCLK01	73
70	PEWAKE# (I/O)(0/3.3V)	REFCLK02	71
68	CLKREQ# (I/O)(0/3.3V)	PET#1	69
66	PERST# (I/O)(3.3V)	PET#1	67
64	RESERVED	PET#1	65
62	ALERT# (I/O)(3.3V)	GN0	63
60	DC_CLK (I/O)(3.3V)	RESERVED/PER#1	61
58	DC_DATA (I/O)(0/3.3V)	RESERVED/PER#1	59
56	V_DISABLE# (I/O)(3.3V)	GN0	57
54	W_DISABLE# (I/O)(3.3V)	PEWAKE# (I/O)(0/3.3V)	55
52	PERST# (I/O)(3.3V)	CLKREQ# (I/O)(0/3.3V)	53
50	USCLK13#H (I/O)(3.3V)	GN0	51
48	COEX1 (I/O)(0/1.8V)	REFCLK00	49
46	COEX2 (I/O)(0/1.8V)	REFCLK00	47
44	COEX3 (I/O)(0/1.8V)	GN0	45
42	VENDOR DEFINED	PET#0	43
40	VENDOR DEFINED	PET#0	41
38	VENDOR DEFINED	GN0	39
36	GN0	PER#0	37
34	DP_M0#	PER#0	35
32	DP_M0#	GN0	33
30	GN0	DP_M#0 (I/O)(0/3.3V)	31
28	DP_M1#	GN0	29
26	DP_M2#	DP_M2#	27
24	GN0	DP_M2#	25
22	DP_A0#	DP_M2#	23
20	DP_A0#	DP_M2#	21
18	GN0	DP_M2#	19
16	LED# (I/O)(0)	DP_M#0# GND (H/L) 3.3V (I/O)(3.3V) (I/O)	17
	Module Key	Module Key	
	Module Key	Module Key	
	Module Key	Module Key	
	Module Key	Module Key	
6	LED# (I/O)(0)	GN0	7
4	1.5V	USB_D-	5
2	1.5V	USB_D+	3
		GN0	1

M2A-VGA
M2A-DP
M2A-eDP/LVDS



2230 Key E+A Wifi+Blue

Table 24. Socket 1 Module Pinout with Dual Module Key (A-E)

74	1.5V	GN0	75
72	1.5V	RESERVED/REFCLK01	73
70	LIM_POWER_SRC/GPIO/PEWAKE#	RESERVED/REFCLK01	71
68	LIM_POWER_SNK/CLKREQ#	GN0	69
66	LIM_SWP/PERST#	RESERVED/PET#1	67
64	RESERVED	RESERVED/PET#1	65
62	ALERT# (I/O)(3.3V)	GN0	63
60	DC_CLK (I/O)(3.3V)	RESERVED/PER#1	61
58	DC_DATA (I/O)(0/3.3V)	RESERVED/PER#1	59
56	V_DISABLE# (I/O)(3.3V)	GN0	57
54	W_DISABLE# (I/O)(3.3V)	PEWAKE# (I/O)(0/3.3V)	55
52	PERST# (I/O)(3.3V)	CLKREQ# (I/O)(0/3.3V)	53
50	USCLK13#H (I/O)(3.3V)	GN0	51
48	COEX1 (I/O)(0/1.8V)	REFCLK00	49
46	COEX2 (I/O)(0/1.8V)	REFCLK00	47
44	COEX3 (I/O)(0/1.8V)	GN0	45
42	VENDOR DEFINED	PET#0	43
40	VENDOR DEFINED	PET#0	41
38	VENDOR DEFINED	GN0	39
36	N/C	PER#0	37
34	N/C	PER#0	35
32	N/C	GN0	33
	Module Key	Module Key	
	Module Key	Module Key	
	Module Key	Module Key	
	Module Key	Module Key	
22	N/C	N/C	23
20	N/C	N/C	21
18	N/C	N/C	19
16	LED# (I/O)(0)	N/C	17
	Module Key	Module Key	
	Module Key	Module Key	
	Module Key	Module Key	
	Module Key	Module Key	
6	LED# (I/O)(0)	GN0	7
4	1.5V	USB_D-	5
2	1.5V	USB_D+	3
		GN0	1



2230 Key E Wifi+Blue

Table 22. SDIO Based Module Solution Pinout (Module Key E)

74	1.5V	GN0	75
72	1.5V	RESERVED/REFCLK01	73
70	LIM_POWER_SRC/GPIO/PEWAKE#	RESERVED/REFCLK01	71
68	LIM_POWER_SNK/CLKREQ#	GN0	69
66	LIM_SWP/PERST#	RESERVED/PET#1	67
64	RESERVED	RESERVED/PET#1	65
62	ALERT# (I/O)(3.3V)	GN0	63
60	DC_CLK (I/O)(3.3V)	RESERVED/PER#1	61
58	DC_DATA (I/O)(0/3.3V)	RESERVED/PER#1	59
56	V_DISABLE# (I/O)(3.3V)	GN0	57
54	W_DISABLE# (I/O)(3.3V)	PEWAKE# (I/O)(0/3.3V)	55
52	PERST# (I/O)(3.3V)	CLKREQ# (I/O)(0/3.3V)	53
50	USCLK13#H (I/O)(3.3V)	GN0	51
48	COEX1 (I/O)(0/1.8V)	REFCLK00	49
46	COEX2 (I/O)(0/1.8V)	REFCLK00	47
44	COEX3 (I/O)(0/1.8V)	GN0	45
42	VENDOR DEFINED	PET#0	43
40	VENDOR DEFINED	PET#0	41
38	VENDOR DEFINED	GN0	39
36	UART_CTS (I/O)(1.8V)	PER#0	37
34	UART_RTS (I/O)(1.8V)	PER#0	35
32	UART_RXD (I/O)(1.8V)	GN0	33
	Module Key	Module Key	
	Module Key	Module Key	
	Module Key	Module Key	
	Module Key	Module Key	
22	UART_TRD (I/O)(1.8V)	SDIO RESET# (I/O)(1.8V)	23
20	UART_WAKE# (I/O)(1.8V)	SDIO WAKE# (I/O)(1.8V)	21
18	PC#0_OUT/IS_SD_OUT (I/O)(1.8V)	SDIO DATA0/I/O(0/1.8V)	19
16	LED# (I/O)(0)	SDIO DATA0/I/O(0/1.8V)	17
14	PC#0_IN/IS_SD_IN (I/O)(1.8V)	SDIO DATA1/I/O(0/1.8V)	15
12	PC#1_OUT/IS_SD_OUT (I/O)(1.8V)	SDIO DATA1/I/O(0/1.8V)	13
10	PC#1_SYNC/IS_WS (I/O)(1.8V)	SDIO CMD0/I/O(0/1.8V)	11
8	PC#1_CLK/IS_SCK (I/O)(1.8V)	SDIO CLK/I/O(1.8V)	9
6	LED# (I/O)(0)	GN0	7
4	1.5V	USB_D-	5
2	1.5V	USB_D+	3
		GN0	1