Содержание

DisplayPort\eDP

ESD

Power

Connectors

Main Link Lane 0+ 1
Main Link Lane 0- 3
Ground 5
Main Link Lane 2+ 7
Main Link Lane 2- 9
Ground 11
Configuration 1 13
Auxiliary Channel+ 15
Auxiliary Channel- 17
Return 19

2 Ground
4 Main Link Lane1+
6 Main Link Lane1 -
8 Ground
10 Main Link Lane3+
12 Main Link Lane3 -
14 Configuration 2
16 Ground
18 Hot Plug Detect
20 Connector Power
(3.3V, 500mA)
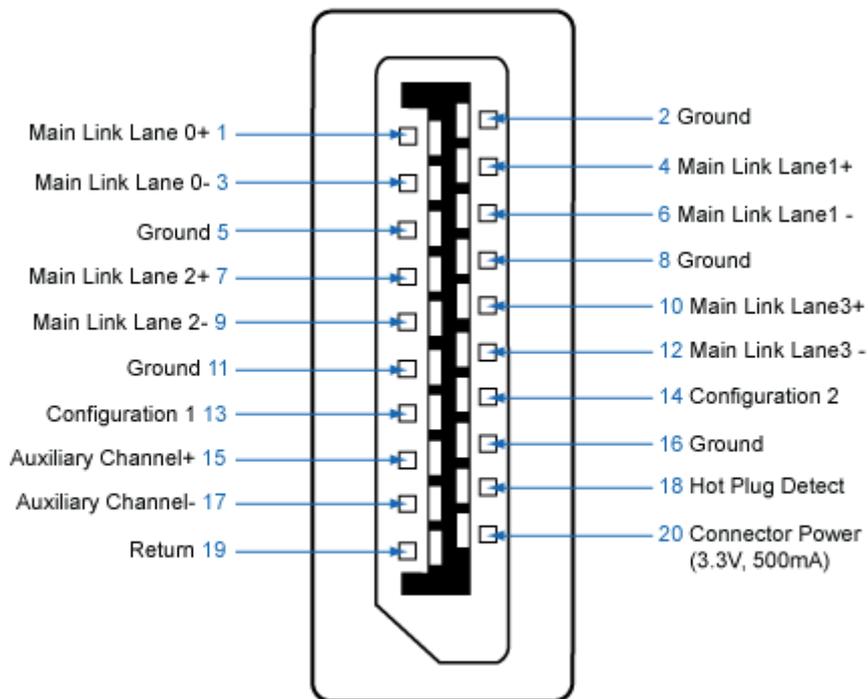
SMARC
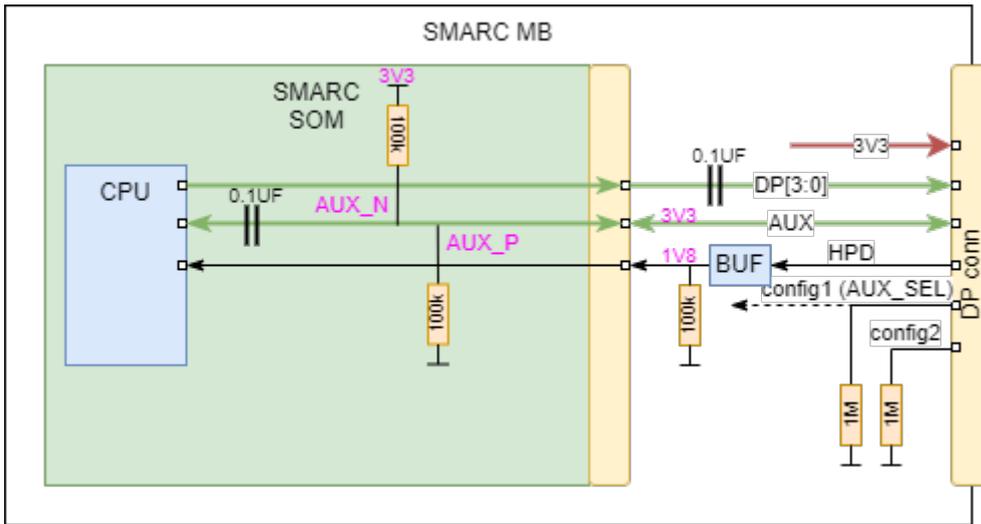
eDP

eDP conn				DP2EDP board	
Terminal	Symbol	Functions			
PIN No.	Symbol	Description			
1	NC	NC			
2	H_GND				
3	Lane3_N				
4	Lane3_P				
5	H_GND				
6	Lane2_N				
7	Lane2_P				
8	H_GND				
9	Lane1_1N	eDP lane			
10	Lane1_1P	Up to 5.4G			
11	H_GND				
12	Lane1_0N				
13	Lane1_0P				
14	H_GND				
15	AUX_CH_P				
16	AUX_CH_N				
17	H_GND				
18	LCD_VCC				
19	LCD_VCC	LCD Logic Powe			
20	LCD_VCC	(3.3±0.3V)			
21	LCD_VCC				
22	LCD_Self_Test(BIST)	BIST（IN Port)			
23	LCD_GND				
24	LCD_GND	Logic GND			
25	LCD_GND	(Connect to GND in M			
26	LCD_GND				
27	HPD	HPD（OUT Port 2.5V/			
28	BL_GND				
29	BL_GND	BLU GND			
30	BL_GND	(Connect to GND in M			
31	BL_GND				
32	BL_ENABLE	IN Port(≥2.5V@High			
33	BL_PWM	IN Port(≥2.5V@High			
34	H_sync	H_sync（OUT Port 2.5V			
35	DBC	Dimming LED backlight			
36	BL_PWR				
37	BL_PWR	BLU Power			
38	BL_PWR	(5~21V)			
39	BL_PWR				
40	Color Engine	IN Port(≥1.8V@High Mode)			