

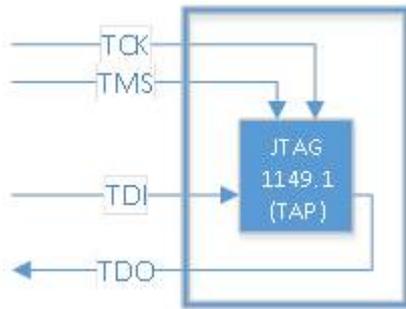
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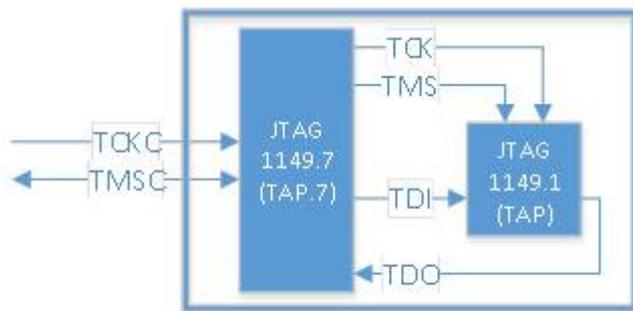


# DEBUG-PROGRAM

## JTAG



## JTAG 2 WIRE



<https://habr.com/ru/articles/237885/>

## Софт для отладки

### OpenOCD

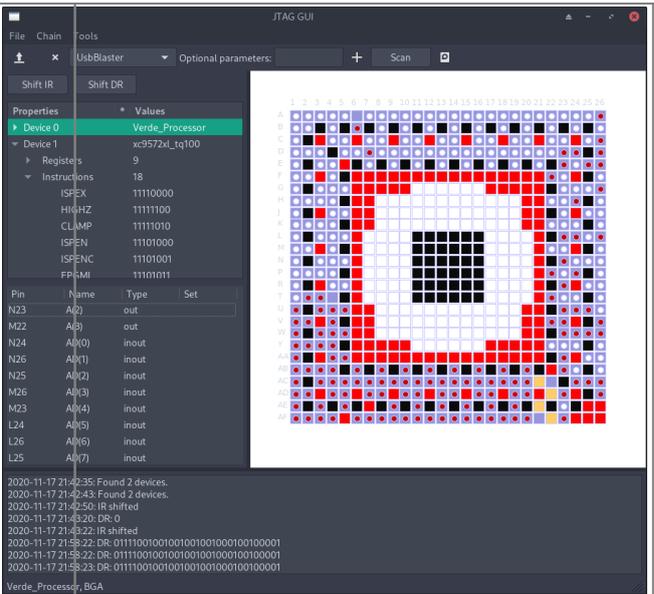
<https://openocd.org/>

В openocd/tcl/target/ti-cjtag.cfg должно быть в начале runtest 20 - иначе не работает на 100kHz.

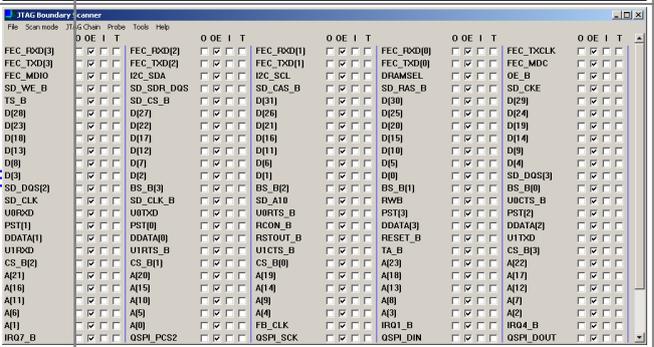
### JTAG tools

<https://github.com/colinoflynn/pyjtagbs>

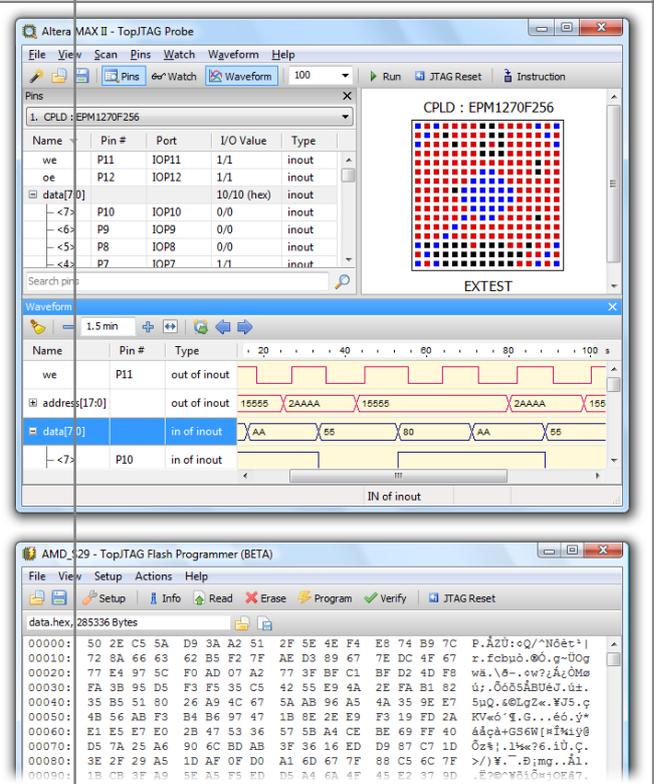
<https://github.com/Najsztub/jtagGUI>

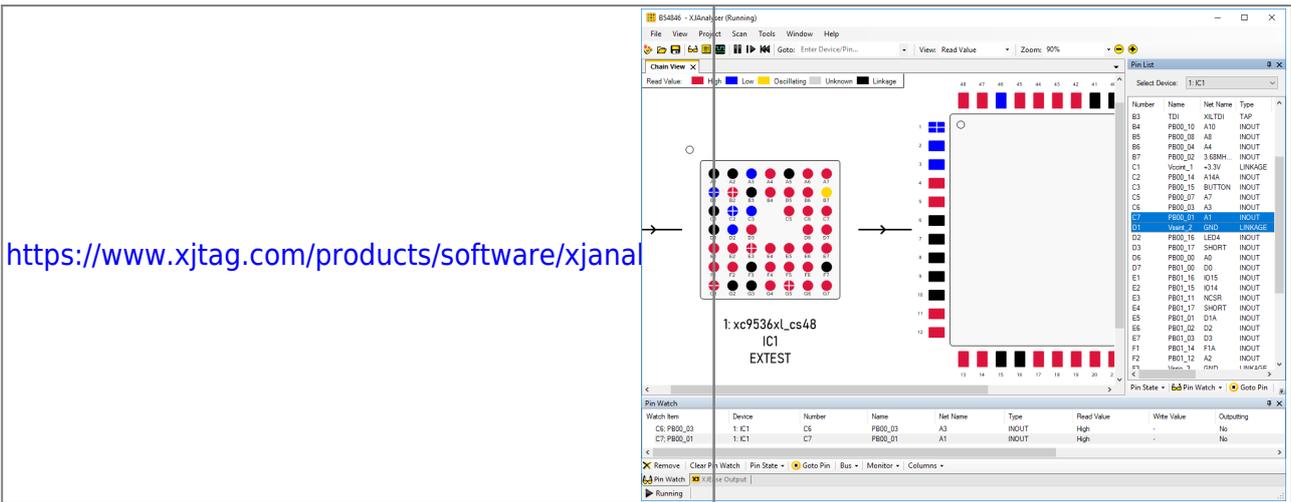


<https://github.com/viveris/jtag-boundary-scanner>



<http://www.topjtag.com/>





## Программаторы

Ссылка на документацию	Название	
<a href="#">JLink</a>	JLink	JTAG, ????
<a href="#">JLink pinout</a>	JLink	JTAG, SWD

## Targets

### TI

CC2652 unbricking with Raspberry Pi and OpenOCD [link](#)

### Buffalo labs

Introduction of OpenOCD and GDB.pdf [link](#)