Содержание

Adlink nanoX-EL

pic:

pn:
: nanoX-EL

formfactor:
: COM Express Type 10

manufacturer:
: Adlink

cpu:
: Intel Atom x6000E

emmc:
: 16/32/64 GB

ram:
: 2/4/8/16 GB LPDDR4

temprange:
: 0...+60 °C, -40...+85 °C

edp:
: 1 (or LVDS)

dp:
: 1 DP/HDMI/VDI

lvd:
: Single Channel via eDP<>LVDS IC (or eDP)

ethernet:
: 1 (2.5 GBit, SGMII) (GPY211/GPY215)

dsi:
: -

csi:
: -

usb2:
: 6(8)

usb3:
: 2

can:
: 1 (in place of UART1)

uart:
: 2 (UART1 or CAN) (from EC, from CPU option)

spi:
: + (BIOS, TPM)

pcie:
: x4 x 1, x2 x 2, x1 x 4 gen 3

i2c:
: 1 (from EC, option from CPU)

smbu:
: 1

gpio:

4 GPI, 4 GPO from EC (or SDIO 3.0 from CPU option)

bmc:

EC (IT5121VG-I-128)

tpm:

TPM 2.0 (Infineon) (option)

sata:

2 SATA III

sdio:

SDIO 3.0 (option) 3.3V only

bio:

AMI UEFI

dimension:

84 x 55 mm

lpc:

+ (eSPI<>LPC)

espi:

-

datasheetwww:

link

datasheet:

link

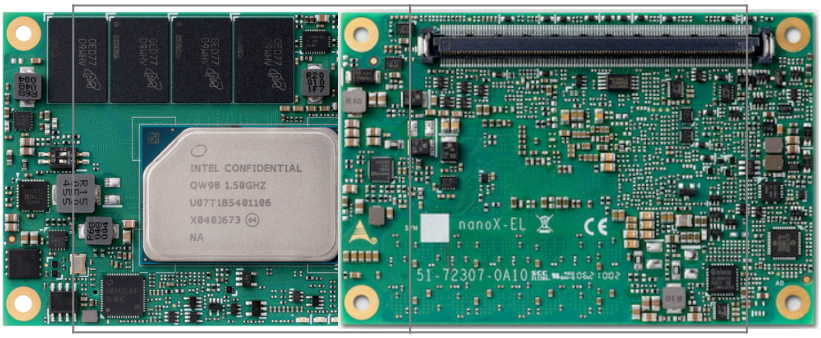
struct:

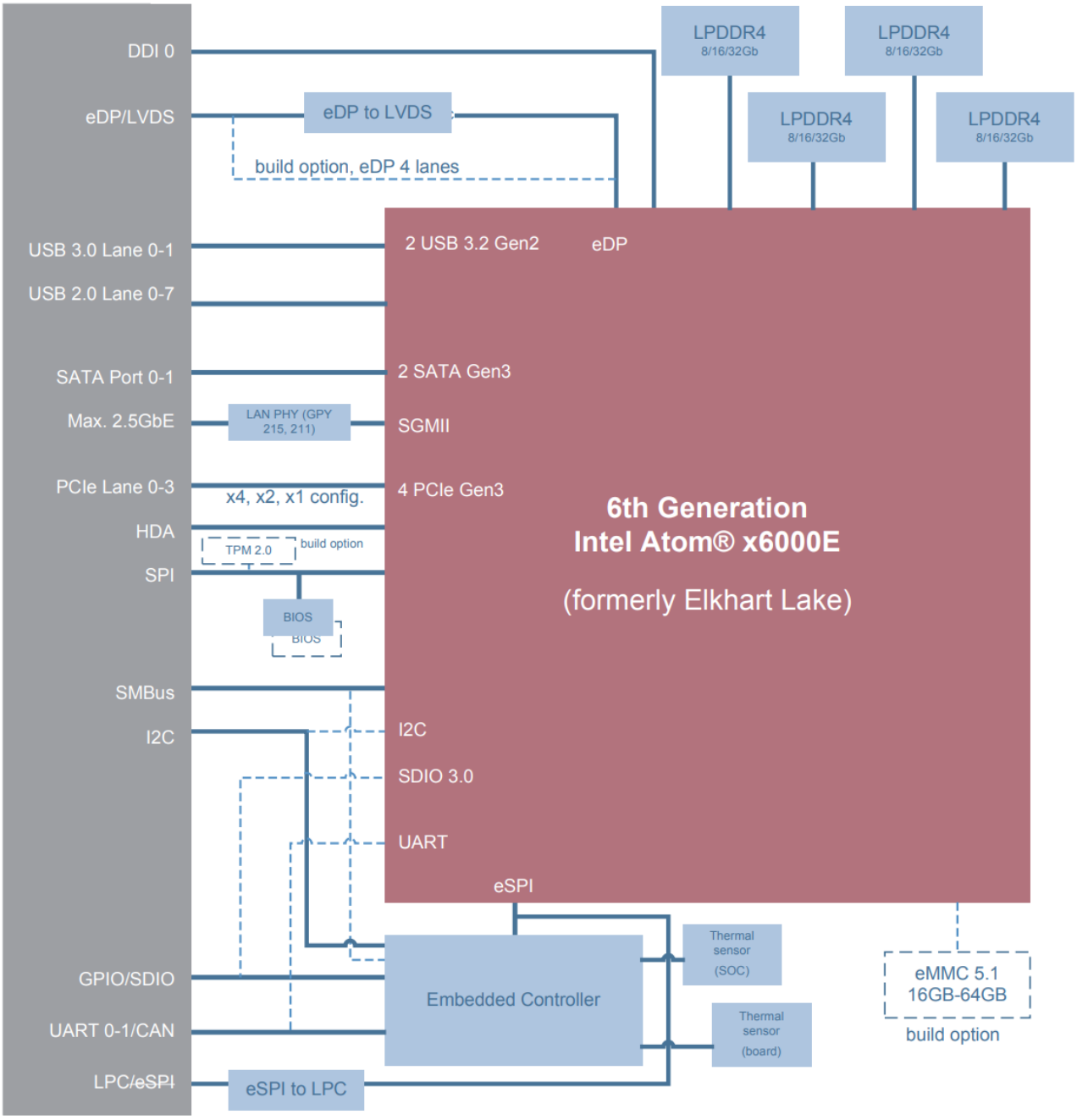
page:

page

power:

ATX 12V/5Vsb, ATX 4.75-20V/5Vsb, AT 12V, AT 4.75-20V





docs

nanoX-EL	Adlink	SOM flyer	link
nanoX-EL	Adlink	User's guide	link
PTN3460	NXP	eDP<>LVDS bridge	link
IT5121VG-I-128	ITE	EC	
NCT5104D	Nuvoton	LPC<>UART	link
ECE1200	Microchip	eSPI<>LPC	link

SATA

SATA0	HSIO 10
SATA1	HSIO 11

PCIe

PCIE0	HSIO 2
PCIE1	HSIO 3
PCIE2	HSIO 4
PCIE3	HSIO 5

LPC

LPC_CLK = 24MHz

LPC Bus is supported by a eSPI-to-LPC bridge IC

USB

USB 0	HSIO 0	from XHCI controller
USB 1	HSIO 1	from XHCI controller

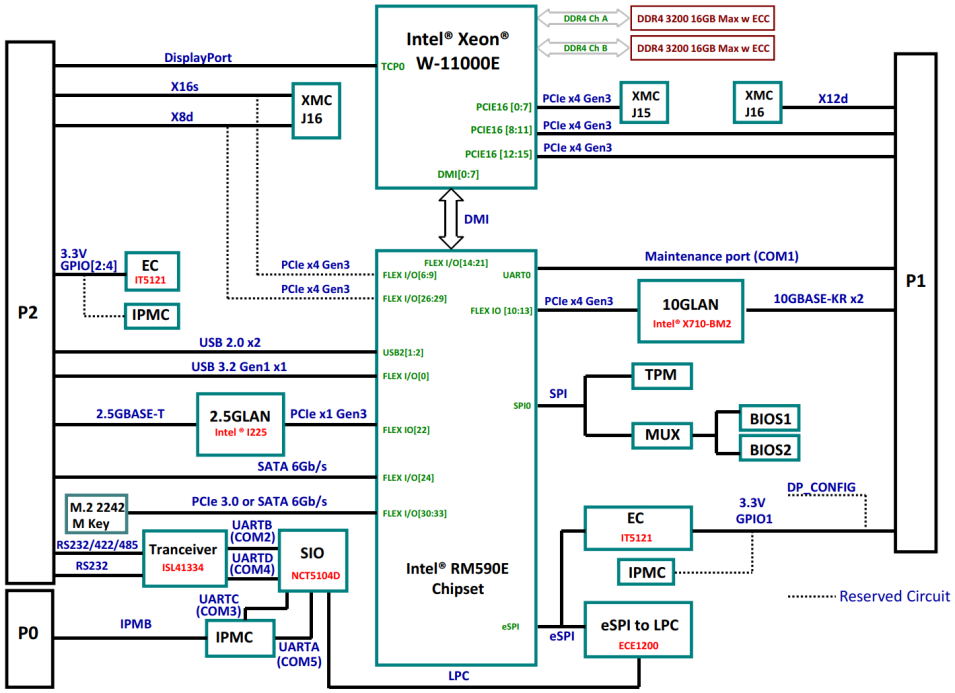


Figure 1-1: VPX3-TL Functional Block Diagram