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Ссылки

Библиотека\набор IP ядер LiteX (python)	https://github.com/enjoy-digital/litex https://habr.com/ru/articles/594817/ https://github.com/litex-hub https://fpga-systems.ru/share/conference/2020/fpga-systems-2020-kaushan.pdf
Язык описания регистров SystemRDL	https://www.accellera.org/downloads/standards/systemrdl https://github.com/SystemRDL https://en.wikipedia.org/wiki/SystemRDL https://www.accellera.org/images/downloads/standards/systemrdl/SystemRDL_2.0_Jan2018.pdf

FPGA Intel

FPGA Intel	https://www.intel.com/content/www/us/en/products/programmable.html	Altera, USA

FPGA Xilinx

https://www.xilinx.com/	AMD, USA

FPGA Lattice

https://www.latticesemi.com/	USA

FPGA Efinix

https://www.efinixinc.com/index.html	USA?

FPGA Gowin

FPGA Gowin	https://www.gowinsemi.com/en/	China